DEFINING LOGIC INSTRUCTIONS

Logic operations such as AND, OR, and NOT, applied to individual bits, are the basic building blocks of digital circuits, as described. It is also useful to be able to perform logic operations is software, which is done using instructions that apply these operations to all bits of a word or byte independently and in parallel. For example, the instruction

Not dst

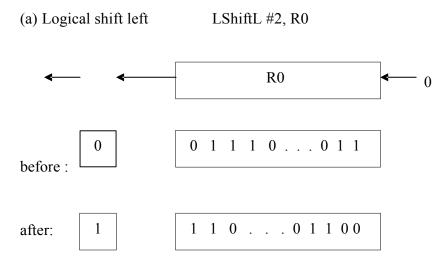
SHIFT AND ROTATE INSTRUCTIONS:-

There are many applications that require the bits of an operand to be shifted right or left some specified number of bit positions. The details of how the shifts are performed depend on whether the operand is a signed number or some more general binary-coded information. For general operands, we use a logical shift. For a number, we use an arithmetic shift, which preserves the sign of the number.

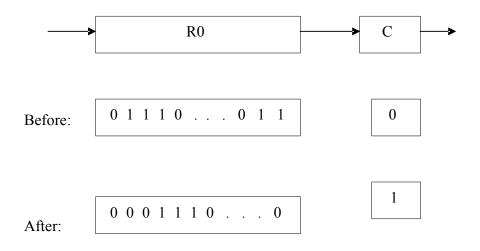
Logical shifts:-

Two logical shift instructions are needed, one for shifting left (LShiftL) and another for shifting right (LShiftR). These instructions shift an operand over a number of bit positions specified in a count operand contained in the instruction. The general form of a logical left shift instruction is

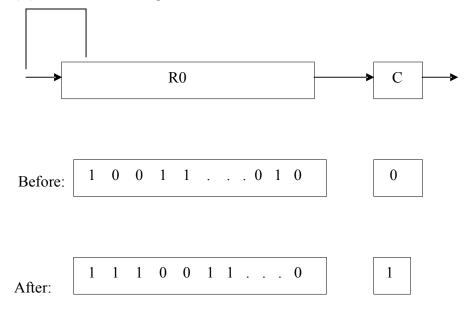
count, dst



LShiftL



(c) Arithmetic shift right AShiftR #2, R0

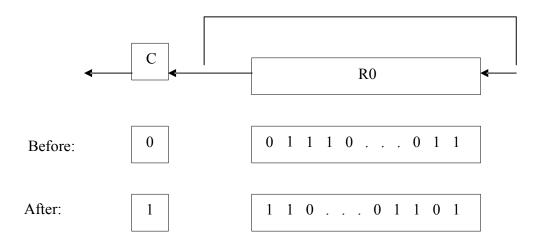


Rotate Operations:-

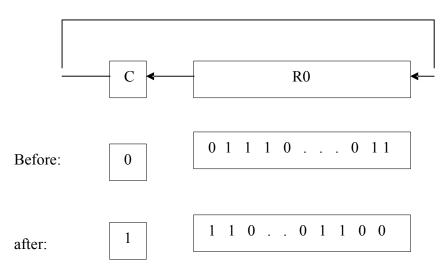
In the shift operations, the bits shifted out of the operand are lost, except for the last bit shifted out which is retained in the Carry flag C. To preserve all bits, a set of rotate instructions can be used. They move the bits that are shifted out of one end of the operand back into the other end. Two versions of both the left and right rotate instructions

are usually provided. In one version, the bits of the operand are simply rotated. In the other version, the rotation includes the C flag.

(a) Rotate left without carry RotateL #2, R0

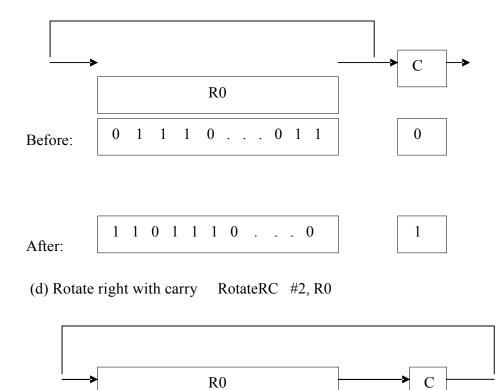


(b) Rotate left with carry RotateLC #2, R0



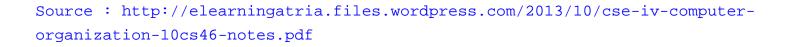
Before:

after:



0 1 1 1 0 . . . 0 1 1

 $1 \ 0 \ 0 \ 1 \ 1 \ 1 \ 0 \ . \ . \ 0$



0

1