

DIGITAL TO ANALOG CONVERTERS

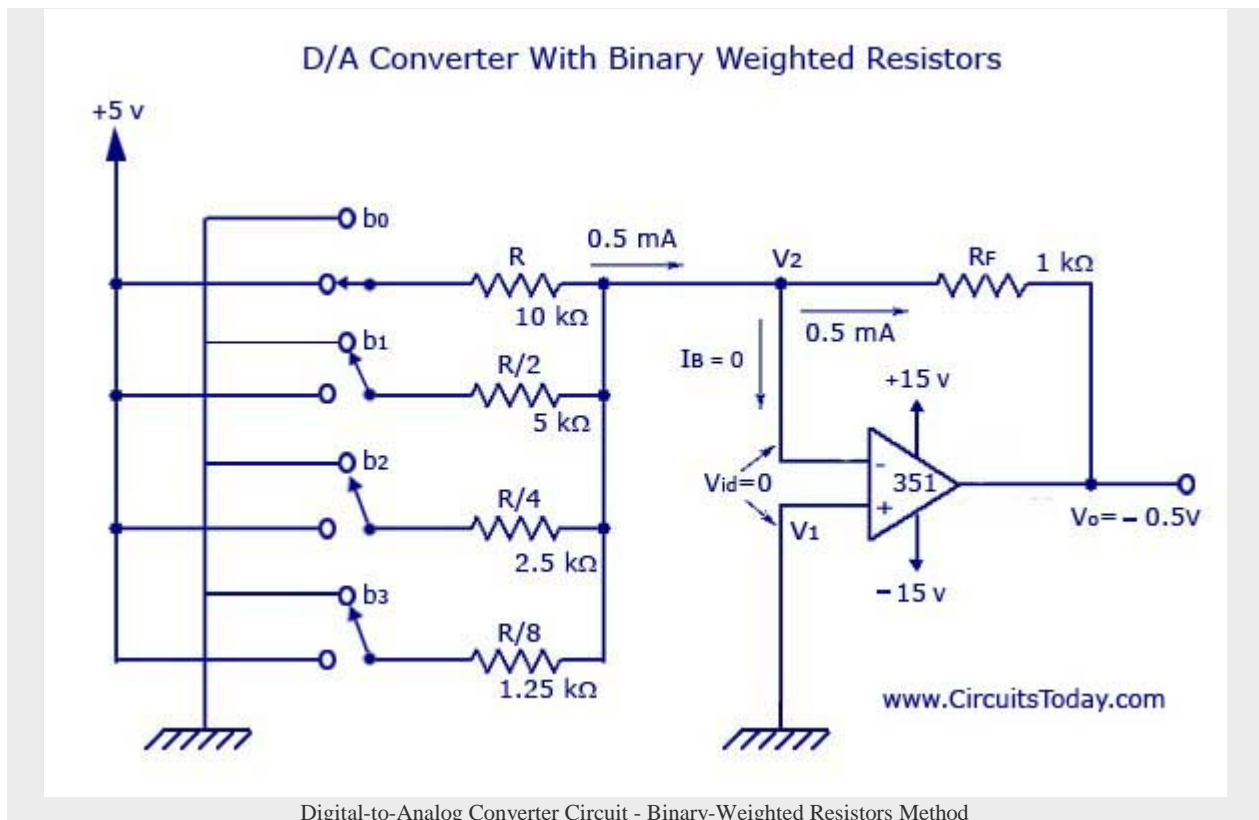
A D/A Converter is used when the binary output from a digital system is to be converted into its equivalent analog voltage or current.

The binary output will be a sequence of 1's and 0's. Thus they may be difficult to follow. But, a D/A converter helps the user to interpret easily.

Basically, a D/A converter has an op-amp. It can be classified into 2 types. They are

1. Digital to Analog Converter using Binary-Weighted Resistors

A D/A converter using binary-weighted resistors is shown in the figure below. In the circuit, the op-amp is connected in the inverting mode. The op-amp can also be connected in the non-inverting mode. The circuit diagram represents a 4-bit converter. Thus, the number of binary inputs is four.



We know that, a 4-bit converter will have $2^4 = 16$ combinations of output. Thus, a corresponding 16 outputs of analog will also be present for the binary inputs.

Four switches from b0 to b3 are available to simulate the binary inputs: in practice, a 4-bit binary counter such as a 7493 can also be used.

Working

The circuit is basically working as a current to voltage converter.

☐ b0 is closed

It will be connected directly to the +5V.

Thus, voltage across R = 5V

Current through R = $5V/10k\Omega = 0.5mA$

Current through feedback resistor, $R_f = 0.5mA$ (Since, Input bias current, I_B is negligible)

Thus, output voltage = $-(1k\Omega) \cdot (0.5mA) = -0.5V$

☐ b1 is closed, b0 is open

$R/2$ will be connected to the positive supply of the +5V.

Current through R will become twice the value of current (1mA) to flow through R_f .

Thus, output voltage also doubles.

☐ b0 and b1 are closed

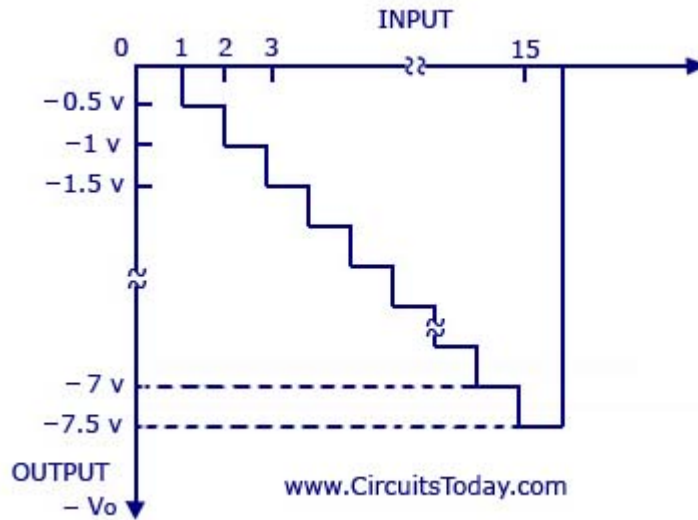
Current through $R_f = 1.5mA$

Output voltage = $-(1k\Omega) \cdot (1.5mA) = -1.5V$

Thus, according to the position (ON/OFF) of the switches (b0-b3), the corresponding “binary-weighted” currents will be obtained in the input resistor. The current through R_f will be the sum of these currents. This overall current is then converted to its proportional output voltage. Naturally, the output will be maximum if the switches (b0-b3) are closed

$V_0 = -R_f \cdot ([b_0/R] + [b_1/(R/2)] + [b_2/(R/4)] + [b_3/(R/8)])$ – where each of the inputs b3, b2, b1, and b0 may either be HIGH (+5V) or LOW (0V).

The graph with the analog outputs versus possible combinations of inputs is shown below.



Digital-to-Analog Converter Circuit - Binary-Weighted Resistors Method Graph

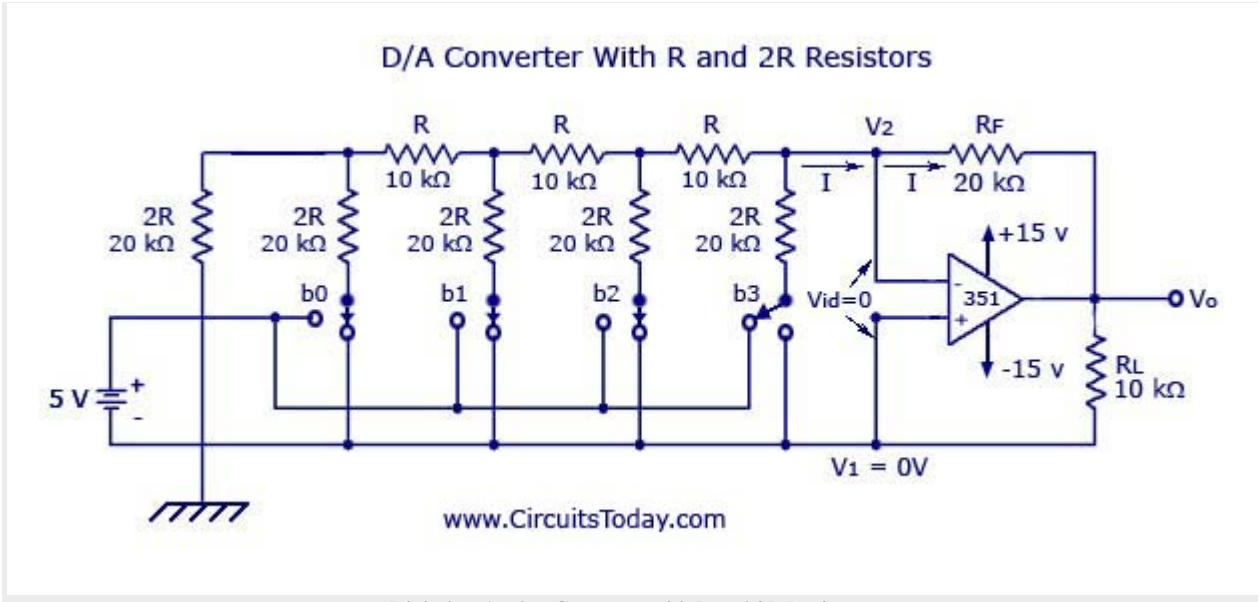
The output is a negative going staircase waveform with 15 steps of $-0.5V$ each. In practice, due to the variations in the logic HIGH voltage levels, all the steps will not have the same size. The value of the feedback resistor R_f changes the size of the steps. Thus, a desired size for a step can be obtained by connecting the appropriate feedback resistor. The only condition to look out for is that the maximum output voltage should not exceed the saturation levels of the op-amp. Metal-film resistors are more preferred for obtaining accurate outputs.

Disadvantages

If the number of inputs (>4) or combinations (>16) is more, the binary-weighted resistors may not be readily available. This is why; R and $2R$ method is more preferred as it requires only two sets of precision resistance values.

2. Digital to Analog Converter with R and $2R$ Resistors

A D/A converter with R and $2R$ resistors is shown in the figure below. As in the binary-weighted resistors method, the binary inputs are simulated by the switches (b_0 - b_3), and the output is proportional to the binary inputs. Binary inputs can be either in the HIGH ($+5V$) or LOW ($0V$) state. Let b_3 be the most significant bit and thus is connected to the $+5V$ and all the other switches are connected to the ground.

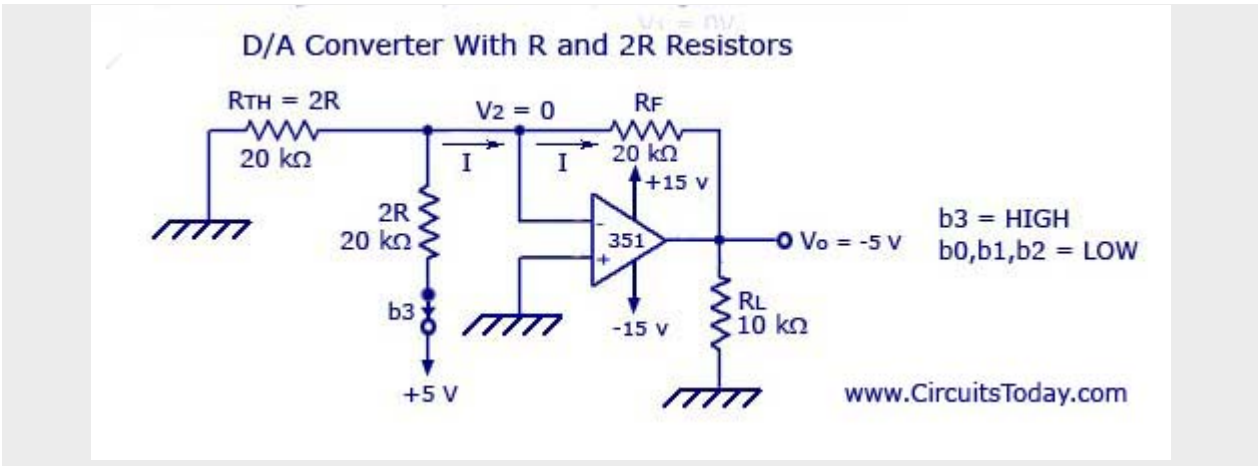


Digital to Analog Converter with R and 2R Resistors

Thus, according to Thevenin's equivalent resistance, R_{TH} ,

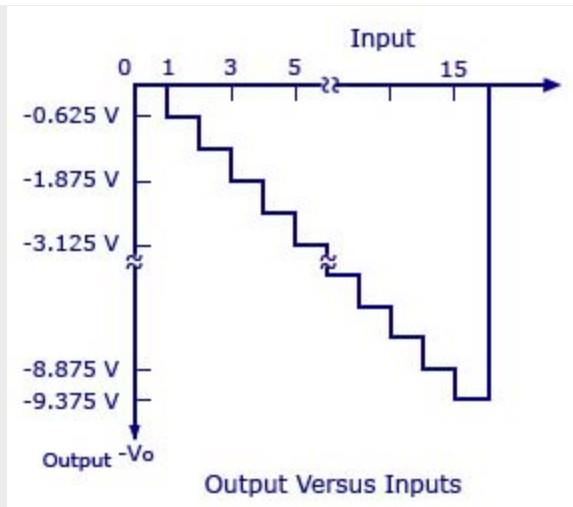
$$R_{TH} = \{ \{ \{ (2R \parallel 2R + R) \} \parallel 2R \} + R \} \parallel 2R + R = 2R = 20k\Omega$$

The resultant circuit is shown below.



Digital to Analog Converter with R and 2R Resistors - Resultant Circuit

Graph is given below.



Digital to Analog Converter with R and 2R Resistors - Graph

In the figure shown above, the negative input is at virtual ground, therefore the current through $R_{TH}=0$.

Current through $2R$ connected to $+5V = 5V/20k\Omega = 0.25 \text{ mA}$

The current will be the same as that in R_f .

$$V_o = -(20k\Omega) \cdot (0.25\text{mA}) = -5V$$

Output voltage equation is given below.

$$V_o = -R_f (b_3/2R + b_2/4R + b_1/8R + b_0/16R)$$

Source : <http://www.circuitstoday.com/digital-to-analog-converters-da>