Shunt active filter algorithms for a three phase system fed to adjustable speed drive

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Abstract—
This paper presents the simulation of Shunt Active Power filter by using two different control schemes, Indirect current control technique and Synchronous detection technique by using ANN. These two control techniques are working under both balanced and unbalanced three phase voltage source conditions and it is feeding to a adjustable speed drive the torque speed characteristics of a motor is presented. Indirect current control technique is implemented under dynamic load condition and load balanced condition. The equal current distribution method of synchronous detection theory is used here to calculate the three phase compensating currents to be provided by the active filter. For calculating sequence currents neural network is used. Neural network is used for estimation of distorted waves, delay less harmonic filtering.

Keywords -- Power Quality, Shunt active filter, indirect current control, Synchronous detection technique using ANN.

1. Introduction

The wide use of non-linear loads such as uninterrupted power supplies (UPS), adjustable speed drives (ASD), furnaces, and single phase computer power supplies etc cause power quality problems such as harmonic currents, poor power factor and voltage sag/swell increase in reactive power. There are several shunt active filtering algorithms developed. This paper presents the latest advanced techniques indirect current control and Synchronous detection technique of the Active filter; the three-phase reference supply currents are obtained using a closed loop PI controller. A Hysteresis PWM current controller is employed over the reference and sensed supply currents to generate gating pulses of IGBT’s of the Active filter. The control algorithm used in this is synchronous detection theory based on Equal current distribution, but this theory, in its original form, will not work under the condition of voltage distortion. This problem can be eliminated by sensing the source/load voltages/currents from which fundamental reference currents can be deduced. By using two control methods the THD values are compared and torque and speed variations for an adjustable speed drive with balanced and unbalanced voltage source are presented here. In this paper, simulation results of indirect current control technique and Synchronous detection technique are present.

2. Indirect current control

The fig: 1 shows the basic control scheme of the AF using indirect current control. Three-phase voltages at PCC along with dc bus voltage of the AF are used for implementation of control scheme.
Comparison of Average value of DC bus voltage ($V_{dc}$) and reference value of dc bus voltage ($V_{dc}^*$) of the AF results in a voltage error, which is fed to a PI controller as shown in figure.

$$V_{dc\ error} = V_{dc}^* - V_{dc}$$

Fig: 1 Control algorithm for indirect current control

Here, proportional ($K_p$) and integral gains ($K_i$) are so chosen, such that a suitable DC bus voltage response is achieved. The output of PI controller is taken as amplitude ($I_{sp}^*$) of the reference supply currents.

Now, Three-phase in-phase components of the reference supply currents are computed using their amplitude and in-phase unit current vectors derived in-phase with the supply voltages, and are given by

$$i_{sa}^* = I_{sp}^* u_{sa}$$
$$i_{sb}^* = I_{sp}^* u_{sb}$$
$$i_{sc}^* = I_{sp}^* u_{sc}$$

Where $u_{sa}$, $u_{sb}$ and $u_{sc}$ are in-phase unit current vectors and are derived as,

$$u_{sa} = v_{sa}/V_{sp}$$
$$u_{sb} = v_{sb}/V_{sp}$$
$$u_{sc} = v_{sc}/V_{sp}$$

Where $V_{sp}$ is the amplitude of supply voltage and it is computed as

$$V_{sp} = \left(\frac{2}{3}(v_{sa}^2 + v_{sb}^2 + v_{sc}^2)\right)^{1/2}$$

Hence from the above procedure, the three phase reference supply currents are computed.

Now the three-phase reference supply currents and sensed supply currents are given as inputs to hysteresis current controller which generates gating signals for IGBT’s of the AF.

A. Implementation of hysteresis based current controller

A Hysteresis current controller is used over reference supply currents ($i_{sa}^*$, $i_{sb}^*$ and $i_{sc}^*$) and sensed supply currents ($i_{sa}$, $i_{sb}$ and $i_{sc}$) to generate gating signals to the IGBT’s used in the VSI bridge working as the AF. In response to gating pulses to the AF, eliminates harmonics, correct the power-factor at PCC to nearly unity and balances the unbalanced nonlinear load while maintaining a self-supporting dc bus of the AF.
The actual current in this forced to track the sine reference within the hysteresis band by back and forth (or bang-bang) switching of the upper and lower switches. The inverter then essentially becomes a current source with peak to peak current ripple, which is controlled within the hysteresis band, which makes the source current to be sinusoidal.

Hence the conditions for switching the devices are,

Upper switch on: \( (i^* - i) > HB \).

Lower switch on: \( (i^* - i) < HB \).

The model of hysteresis controller, which is implemented in MATLAB for the current control purpose, is shown in below figure.

The simulation model for the indirect current control is shown in fig: 2. Three phase supply is used under both balanced and unbalanced conditions. Unbalanced case fifth and seventh harmonics are high. Third harmonic content was zero because of tree phase. The input inductor, \( L_\text{c}=3.35\text{mH} \), \( C_{\text{dc}}=1500\mu\text{F} \). The torque speed characteristic of adjustable speed drive is verified in with both balanced and unbalanced condition.

Fig: 2 Simulation model for Indirect current control scheme in SIMULINK environment.

3. Synchronous detection technique

Synchronous detection (SD) theory can work effectively under balanced as well as unbalanced source and load conditions because the compensating currents are calculated taking into account the magnitudes of per phase voltages. The synchronous detection method is applied to calculate compensating currents while the three phase source is feeding a highly non-linear load. The equal current distribution method of synchronous detection theory is used here to calculate the three phase compensating currents to be provided by the active filter.

The following assumptions are made in calculating the three phase compensating currents using equal current distribution method of synchronous detection algorithm: (i) Voltage is not distorted; (ii) loss in the neutral line is negligible. The equal current synchronous detection method shows a better profile of source side line current after compensation.

Assume the peak values of source currents are balanced after Compensation:

\[
I_{am} = I_{bm} = I_{cm} = I_m
\]

Peak values of active current in each phase after compensation are
Where \( P_a, P_b \) and \( P_c \) are real powers from each of the phases and \( V_{am}, V_{bm} \) and \( V_{cm} \) are peak values of phase voltages in the three phases. From the above equations

\[
\frac{2P_a}{V_{am}} = \frac{2P_b}{V_{bm}} = \frac{2P_c}{V_{cm}}
\]

Then

\[
P_b = \frac{V_{bm}}{V_{am}} P_a
\]

\[
P_c = \frac{V_{cm}}{V_{am}} P_a
\]

The total average power

\[
P_{av} = P_a + P_b + P_c
\]

By rearranging

\[
P_a = \frac{V_{am}}{V_t} P_{av}
\]

\[
P_b = \frac{V_{bm}}{V_t} P_{av}
\]

\[
P_c = \frac{V_{cm}}{V_t} P_{av}
\]

The reference active source currents are calculated as

\[
i_{ac e}(t) = \frac{2p_{av}}{V_{am} * V_t} v_{an}(t)
\]

\[
i_{bc e}(t) = \frac{2p_{av}}{V_{bm} * V_t} v_{bn}(t)
\]

\[
i_{cc e}(t) = \frac{2p_{av}}{V_{cm} * V_t} v_{cn}(t)
\]

Where

\[V_t = V_{am} + V_{bm} + V_{cm}\]

The compensating current is obtained as

\[
i_{can}(t) = i_{an}(t) - i_{ac e}(t)
\]

\[
i_{cbn}(t) = i_{bn}(t) - i_{bc e}(t)
\]

\[
i_{ccn}(t) = i_{cn}(t) - i_{cc e}(t)
\]

In the control scheme, the fundamental positive sequence currents are being extracted. The reference currents can be obtained by subtracting the positive sequence current from the load current. The online extraction can be done by using Fourier integral or moving average filtering method. These methods involve a considerable delay for the calculation of Fourier coefficients. This delay can be reduced by the use of neural network filter. Applications of neural networks in power electronics are estimation of distorted waves, FFT analysis, on-line diagnostics, PWM techniques, delay less harmonic filtering, adaptive P-I close loop control etc. The control scheme applied in this work uses the neural network filter for online extraction of sequence currents.

Artificial neural network (ANN) is an emerging technology that is advancing rapidly and having impact on many engineering applications.
Neural Network Structure

The basic element needed for neural network is the neuron. The Structure of an artificial neuron is shown in fig: 3.

In the neuron the input signals pass through synaptic weights and collect in the summing node before passing through a transfer function at the output. A bias signal can be added in the summing node.

The interconnection of artificial neurons results in an artificial neural network (ANN). The ANN can generally be classified as feed forward and feedback types. In a feed forward network, the signals from neuron to neuron flow only in forward direction, whereas in feedback network the signals flow in forward as well as backward direction. The examples of feed forward networks are perceptron, Adaline and Madaline and Back propagation network. The examples of feedback networks are Hopfield network and Boltzmann network.

Extraction of negative sequence currents: For the extraction of sequence currents a three layer feedforward back propagation neural network is used. The general structure of a feed-forward back propagation network is shown below. The name "back propagation" comes from its characteristic training method. For extraction of sequence currents a three layer feed-forward back propagation neural network is used, with ten neurons in first layer (input layer) and ten neurons in second layer (hidden layer), three neurons in third layer (output layer). The transfer functions for all layers are purelin.

Training Data generation: For the extraction of sequence currents the required inputs are three phase currents, and the corresponding outputs. The required inputs and outputs are generated in MATLAB program. In the program the input Currents are initialized to zero, and incremented in steps. By taking one thousand samples in a cycle and arranging them in a vector of three rows, outputs also arranged in required vector size depends on the no of the outputs. The input vector size and output vector size must have same no of columns. The training data is generated for both balanced and unbalanced conditions. The number of the data required is depending on the network architecture and required error tolerance.

Training the Network: The training of the network was done with MATLAB program. The training data required the number of epochs, error and min_gradient. The number of epochs depends on the error, and architecture of the network. First, the weights are initialized to random numbers. After training the first data the weights are adjusted to the required outputs. The training is done for both balanced and unbalanced conditions. After training, the network is simulated with trained inputs. If the network errors are with in the predefined range then the architecture is suitable, otherwise the network architecture must be changed. After successful training the network can also give the required outputs with unknown inputs. After training the architecture is converted to simulink block.

The generation of training data and training the neural network is done in using the neural network toolbox commands. The neural network structure is converted into simulink diagram using the command gensim, to interface with the other power system blocks. The fig:5 shows the 3 layers of the neural network and the fig:6
shows the inside view of a layer. The fig:8 shows the no of the neurons of a layer. Simulation model for the synchronous detection technique is shown in fig: 7.

Fig: 5 3Layer Feed-forward Neural Network

Fig: 6 view of single Layer

Fig: 7 Simulation Model for Shunt active Filter for proposed Synchronous detection method

Fig: 8 View of Weights of the layer one
4. Simulation results

The simulation results of the shunt active power filter is carried in MATLAB/Sim Power Systems environment. The simulation results are shown below. As can be seen from the figure, the non-linear load is a three-phase bridge rectifier feeding an adjustable speed drive. The simulation is done for various source and load conditions for providing harmonic compensation, load balancing and reactive power compensation. It can be clearly seen that by using Synchronous detection technique THD value is less compared to indirect current control technique.

Fig: 9 Indirect current control wave forms under balanced three phase
(a) Supply voltage& current (b) load current (c) filter current

Fig: 10 Indirect current control wave forms under dynamic Load condition load is changing from 5KW to 10KW to 5KW. Three phase (a) supply voltage& current (b) Load current
Fig: 11 power factor is unity for supply voltage and current

Fig: 12 speed & torque wave forms of adjustable speed drive for Indirect current control

Fig: 13 Indirect control FFT analysis for supply current

Fig: 14 Indirect control FFT analysis for load current & filter current
Fig: 15 Synchronous detection wave forms under balanced voltage. Three phase (a) supply voltage (b) supply current (c) Load current (d) output voltage (e) filter current.

Fig: 17 Speed & torque wave forms of adjustable speed drive (Synchronous detection).

Fig: 18 Synchronous detection wave forms under unbalanced voltage. Three phase (a) supply voltage (b) supply current (c) Load current (d) supply voltage after balancing (e) filter current.

Fig: 19 FFT analysis for supply current (Synchronous detection).
Table 1 shows that by using indirect current control the THD value is high compared to Synchronous detection technique. The harmonic content was eliminated in Synchronous detection technique is high.

5. Conclusion

In this paper, indirect current control and Synchronous detection methods using equal current division technique have been applied to a shunt active power filter to compensate for reactive and harmonic currents under balanced and unbalanced source voltage conditions. The simulation has been carried out in MATLAB/SIMULINK environment and power factor is unity for supply voltage, current.

6. References